

	Institute of Engineering & Technology, DDU Gorakhpur University Timetable of B.Tech. Programme Session 2022-23					
	Year: 2 nd	Semester:3 rd		W.e.f.: 16-07-22	Branch: Information Technology	
Time Day	10:00AM- 11:00AM	11:00AM- 12:00AM	12:00PM- 01:00PM	01:00PM- 02:00 PM		03:00PM- 04:00PM 04:00PM- 05:00PM
Monday	DS RRKT (IET-211)	DSTL SPS (IET-211)	DLD MSC (IET-211)	LIBRARY		P1/SL1 Lab/SPS P2/CO Lab/UNT
Tuesday	DSTL SPS (IET-211)	DS RRKT (IET-211)	COA SPS (IET-211)	DLD MSC (IET-211)		P2/SL1 Lab/SPS P1/CO Lab/UNT
Wednesday	MATH-III JS (IET-211)	UHV&PE RKS (IET-211)	DS RRKT (IET-211)	DSTL SPS (IET-211)		P1/DSUC Lab/RRKT P2/DLD Lab/MSC
Thursday	MATH-III ASB (IET-211)	UHV&PE RKS (IET-211)	COA SPS (IET-211)	DS RRKT (IET-211)		P2/DSUC Lab/RRKT P1/DLD Lab/MSC
Friday	MATH-III RK (IET-211)	UHV&PE RKS (IET-211)	COA RRKT (IET-211)	DLD MSC (IET-211)		TECHNICAL PRESENTATION (P1) LIBRARY (P2)
Saturday	MATH-III GPS (IET-211)	LIBRARY	TECHNICAL PRESENTATION (P2) LIBRARY (P1)			SPORTS /CULTURAL ACTIVITY
Subject			Code	Credits	Faculty	
Mathematics-III (MATH-III)			GAS301	4	JS: Dr. Julee Srivastava ASB: Dr. Archana Singh Bhadauria RK: Dr. Rajesh Kumar GPS: Dr. Gyanvendra Pratap Singh	
Universal Human Values & Professional Ethics (UHV&PE)			GVE301	3	RKS: Dr. Rajesh Kumar Singh	
Digital Logic Design (DLD)			GEC301	3	MSC: Dr. Madan Singh Chauhan	
Discrete Structures & Theory of Logic (DSTL)			GCS301	3	SPS: Dr. Suryabhan Pratap Singh	
Computer Architecture and Organization (CoA)			GCS302	3	RRKT: Mr. Rajeev Ranjan Kumar Tripathi; SPS: Dr. Suryabhan Pratap Singh	
Data Structure (DS)			GCS305	4	RRKT: Mr. Rajeev Ranjan Kumar Tripathi	
Digital Logic Design Lab (DLD Lab)			GEC351	1	MSC: Dr. Madan Singh Chauhan	
Software Lab 1 (SL1 Lab)			GCS351	1	SPS: Dr. Suryabhan Pratap Singh	
Computer Organization Lab (CO Lab)			GCS352	1	UNT: Dr. Upendra Nath Tripathi	
Data Structure Using C Lab (DSUC)			GCS355	1	RRKT: Mr. Rajeev Ranjan Kumar Tripathi	

COORDINATOR

(IET)