

# Brief Curriculum-Vitae

## Dr. Deepak Kumar Singh

B. Tech (ECE), M. Tech (ECE), Ph. D

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**Life disciplines:** Punctual, dedicated towards responsibilities, hard-working, supportive, positive reinforcement.

**Teaching Experience:** 10 years of teaching experiences (Regular on Contract as Assistant Professor – Purvanchal University Jaunpur (06 Years), AKTU (04 Years)) in reputed organizations at UG and PG levels.

### Academic Qualifications:

S. No.	Qualification	Board/University	Specialization	Year
1	Ph. D.	VBS Purvanchal University, Jaunpur, U.P.	Electronics Engineering, <b>Title: <i>Performance Investigations on Free Space Optical (FSO) Communication Link</i></b>	Nov. 2023
2	M. Tech.	AKTU, Lucknow	Electronics & Communication Engineering	2017
3	B. Tech.	AKTU, Lucknow	Electronics & Communication Engineering	2010
4	Intermediate	U.P. Board	PCM	2004
5	High-School	U.P. Board	Science Group	2002

### Interests & Specialization:

- Free Space Optical Communication
- Fiber-Optic Communication
- Engineering Electromagnetic

### Journal/Conference Publications (Total Research Publications: 28)

S. N.	Research Type	Quantity	Author Role	SCI/Scopus/ /UGC Care/Peer Review	Impact Factor	Year of Publications
1	International Journal	02	First/Sole Author	SCI/SCIE	2.3 & 4.2	2023 (02)
2	International Journal	05	First/Sole Author (03) Co-author (02)	Scopus/WoS	2- 2.1	2022 (02), 2023 (01), 2024 (02)
3	International Journal	02	First/Sole author	UGC Care	-	2022 (02)
4	Book (Free Space Optics : A Technology on Verge)	01	Sole author	International (LAMBERT Academic Publishing) ISBN: 978-620-8-42365-0	-	2025
5	International Conference Publications (Proceedings)	04	First/Sole author	IEEE Indexed	-	2021 (01), 2022 (02), 2025 (01)

6	International Conference Publications (Proceedings)	04	First/Sole author	Springer (Lecture Notes Series), T & F CRC Press	-	2022 (02), 2023 (02)
7	Conference Proceeding	04	First/Sole author	Scopus Indexed	-	2023-2025
8	Poster Paper	04	Sole author	01 (IIT ROORKEE) 01 (IIT DELHI & OSI) 01 (Vancouver, Canada) 01 (U.S.A.)	-	2020 (01) 2021 (01) 2022 (02)
9	Book Chapter	01	First/Sole author	Taylor & Francis	-	2023
10	Invited Talk (Full Paper in Proceeding)	01	Invited speaker	Paris, France	-	2025

### Refresher Program Attended Details: (Out Side the University Only)

**Participated** in 16 national & international conferences/workshops at IITs, IEEE and **Attended** 30+ trainings, short term courses, workshops, symposium and FDPs programs at IITs, IIIT, IEEE Society and Industries all over country.

S. No.	Name of the Program	Duration	Place/Indexing	Duration/Year
1	International Conference (Presented Paper) - 14	2-3 Days	IITs, NITs, IEEE, Springer	2019-2024
2	AICTE approved FDPs - 16	One week	AICTE FDPs (ATAL)	2020-2025
3	Workshops/Karyashala - 09	One week	IIT Indore, IIT Mumbai, IIT Delhi	2019 – 2024
4	Symposium/Short Team Courses (STC) - 05	One week	IITs, NITs, State Universities	2019-2024

### Other Achievements:

- A project proposal to establish the **Virtual Lab** on “**Microprocessor & Integrated Circuit Lab**” worth Rs. 30 Lacs has been submitted to Govt. of Uttar Pradesh.
- **Formulated a MoU** with Petrasys Global: Networking & Security Solution, Mumbai in 2022.
- Received **1<sup>st</sup> Ph. D. Degree** in the Department of Electronics Engineering, Faculty of Engineering & Technology, VBS Purvanchal University, Jaunpur, U. P.
- Received **Appreciation Letter** to organize the International Conference ICEECS-2020 at UNSIET VBSPU Jaunpur.
- Received **Appreciation Letter** to organize the National Seminar-HR Conclave.
- Received **Appreciation Letter** for contribution in Admission & Career Counselling.
- Received **Appreciation Letter** from **Head**, Electronics Engineering.
- Received **Best Paper Award** in International Conference.
- Two times **GATE qualified** in the years 2019 & 2021.
- Received **best reviewer's certificate**; Reviewed more than 10 research papers and Books.
- Authored **01 Book** and **01 Book's Chapter** publications.
- Delivered an **Invited talk** at Int. Conf. on Optics and Laser Technology held in Paris, France (Oct. 24-25, 2024).

### Academic Support:

- ❖ **Academic Coordinator**, Department of Electronics Engineering, Faculty of Engineering and Technology, VBS Purvanchal University, Jaunpur (2019-23).
- ❖ **In-charge, Fiber Optics & Photonics Research Lab**, Department of Electronics Engineering, VBS Purvanchal University, Jaunpur.
- ❖ **03 M. Tech.** (Dissertations) and more than **20 B. Tech. Projects** Guided.
- ❖ Developed **21 E-content modules** on Optical Communication and Analog Electronics based on ICT learning on Digital Library, Higher Education, Govt. of Uttar Pradesh.
- ❖ **Member, Syllabus Modification & BoS**, Department of Electronics Engineering, VBS Purvanchal University, Jaunpur.

- ❖ **Class Counsellor (B. Tech-3<sup>rd</sup> Year)** and **Mentor (M. Tech 2<sup>nd</sup> Year)** at Department of Electronics Engineering, VBS Purvanchal University, Jaunpur.
- ❖ **B. Tech. Project Coordinator (Final Year, ECE)** at Department of Electronics & Communication Engineering, DDU Gorakhpur University, Gorakhpur.

#### Administrative Support:

- ❖ **Coordinator NBA**, Department of Electronics Engineering, VBS Purvanchal University, Jaunpur.
- ❖ **Member Team NAAC** accreditation process, VBS Purvanchal University, Jaunpur.
- ❖ **Warden, Charak Boys Hostel**, VBS Purvanchal University, Jaunpur
- ❖ **Co-coordinator In-charge, Central Training & Placement Cell**, VBS Purvanchal University Jaunpur.
- ❖ **Co-Coordinator In-charge, ERP Module 27 (I-Boss)**, VBS Purvanchal University Jaunpur, U.P.
- ❖ In-charge, **E-Samarth Portal**, Department of Electronics Engineering, VBS Purvanchal University, Jaunpur, U.P.
- ❖ **Coordinator, NIRF Ranking and AICTE Approval Process**, Department of Electronics Engineering, Faculty of Engineering and Technology, VBS Purvanchal University, Jaunpur.
- ❖ **Deputy-Nodal Officer**, Central Training & Skill Development Cell, VBS Purvanchal University, Jaunpur.

#### Program Organized:

- ❖ Organized Workshop on **Knowing IPR & plagiarism during research and patents** during 20- 23, Sept 2018.
- ❖ Organized workshops on **Outcome Based Education (OBE)** during 09-10 & 12-14 October, 2018.
- ❖ Organized FDP on **Recent Advances in Communication & Photonics** during July 08-12, 2019.
- ❖ Organized International Conference on **Recent Trends in Electrical, Electronics & Computer Science Engineering (ICEECS)** during January 10-11, 2020.
- ❖ Organized Workshop on **“RF Hazard in 5G Cellular Communication”** on 11 October, 2021 by DOT, Lucknow.
- ❖ Organised **Induction program** for 3 weeks for 1<sup>st</sup> year students during 14, August – 07, September 2019.
- ❖ Organized One Day Workshop & Hand-on session on **“Virtual Lab”** on Sept. 19, 2022 by IIT Delhi.
- ❖ Organized **In-house Summer Workshop on Fiber Sensor: Networking & System security** for B. Tech ECE students during June 14-15, 2022 under the aegis of MoU with **Petrasys Global, Mumbai**.
- ❖ Organized One Day Webinar on **“New Education Policy (NEP)-2020: Aspects of Interdisciplinary Education”** on 28 Feb., 2021
- ❖ Organized and coordinated **Science Day/Engineer’s Day Celebration** program in each year.
- ❖ Organized many **Expert-Talk** on current subject topics.
- ❖ Organized **GATE classes** for final year students.

#### Role as Department Faculty:

Quality academic works will be taken up to strengthen learner centric education, teaching, inter and multi-disciplinary approach in research activities, skills development as well as placement activities in the Department. The knowledge from the undergraduate and postgraduate students will be tapped for mutual benefits through their interests and hard work in the projects. Students will be encouraged to publish their quality research and inclusive approach with the students will be applied towards interaction at international level by organizing international conferences to encourage academic research and internationalization. Industrial exposure for faculties and students will be devised. Students will be exposed to innovative and entrepreneurial activities. ICT facilities will be created alongwith advanced digital learning tools, teaching pedagogy and teaching resources as well as various e-platforms for learning will be devised for students and will work for encouraging creation of electronic platform of learning. Remedial classes for slow learners will be arranged which will be implemented through additional guidance and mentoring support. Funding agencies will be approached for grants/research projects to carry on quality research and create suitable academic infrastructure as well as strengthen of research laboratories. Interaction with Institutions, organizations and Industries will be strengthened through MoUs and suitable consultancy works will be taken up to consider and solve the problem of stakeholders. In total, a holistic approach will be followed for quality teaching and research and overall development of the students.

(Dr. Deepak Kumar Singh)